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## NOTICE OF ALLOWANCE AND FEE(S) DUE

22850

1940 DUKE STREET

7590

12/16/2009

OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L.P. ALEXANDRIA, VA 22314

**EXAMINER** VLAHOS, SOPHIA ART UNIT PAPER NUMBER

2611

DATE MAILED: 12/16/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/555,096	11/02/2005	Yoshiyuki Kajiwara	280664US6PCT	9435

TITLE OF INVENTION: SIGNAL PROCESSING DEVICE AND METHOD, AND SIGNAL DECODING DEVICE AND METHOD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/16/2010

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

#### HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current **SMALL ENTITY status:** 

A. If the status is the same, pay the TOTAL FEE(S) DUE shown

B. If the status above is to be removed, check box 5b on Part B -Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

#### PART B - FEE(S) TRANSMITTAL

#### Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450

Alexandria, Virginia 22313-1450 (571)-273-2885 or <u>Fax</u>

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

Note: A certificate of mailing can only be used for domestic mailings of the CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission. 22850 12/16/2009 Certificate of Mailing or Transmission OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L. Phereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below. 1940 DUKE STREET ALEXANDRIA, VA 22314 (Depositor's name (Signature (Date APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 10/555,096 11/02/2005 Yoshiyuki Kajiwara 280664US6PCT 9435 TITLE OF INVENTION: SIGNAL PROCESSING DEVICE AND METHOD, AND SIGNAL DECODING DEVICE AND METHOD APPLN. TYPE SMALL ENTITY ISSUE FEE DUE PUBLICATION FEE DUE PREV. PAID ISSUE FEE TOTAL FEE(S) DUE DATE DUE nonprovisional NO \$1510 \$300 \$0 \$1810 03/16/2010 **EXAMINER** ART UNIT CLASS-SUBCLASS VLAHOS, SOPHIA 2611 375-232000 1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. or agents OR, alternatively, (2) the name of a single firm (having as a member a ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required. registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) 4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) 4a. The following fee(s) are submitted: lssue Fee A check is enclosed. Publication Fee (No small entity discount permitted) Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number \_\_\_\_\_\_ (enclose an extra copy of this fo Advance Order - # of Copies \_ (enclose an extra copy of this form). 5. Change in Entity Status (from status indicated above) ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2). a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office. Authorized Signature Date Typed or printed name Registration No. This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

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22850 75	590 12/16/2009		EXAMINER		
OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L.P. 1940 DUKE STREET			VLAHOS, SOPHIA		
			ART UNIT	PAPER NUMBER	
ALEXANDRIA, VA 22314		2611			
			DATE MAILED: 12/16/2009		

### Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 790 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 790 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)			
	10/555,096	KAJIWARA, YOSHIYUKI			
Notice of Allowability	Examiner	Art Unit			
	SOPHIA VLAHOS	2611			
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate communication. This application is su	this application. If not included nication will be mailed in due course. <b>THIS</b>	⁄e		
2. $\boxtimes$ The allowed claim(s) is/are <u>1,3-5,7,9-11 and 13</u> .					
3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a)					
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Su Paper No./N 7. ☐ Examiner's A	Mail Date Amendment/Comment  Statement of Reasons for Allowance			

Application/Control Number: 10/555,096 Page 2

Art Unit: 2611

#### **DETAILED ACTION**

#### Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 11/04/2009 has been entered.

### Allowable Subject Matter

2. The following is an examiner's statement of reasons for allowance:

The prior art of the record fails to teach or suggest alone or in combination: A signal processing apparatus, comprising: a multiplier configured to multiply the signal output from the one of said plurality of multiplication units and a signal output from each of said one or more delay units, each with a preset coefficient, and an adder configured to sum outputs of said multiplier together, wherein a step gain parameter for updating each preset coefficient of a multiplier of the remaining one of the said plurality of multiplication units is twice a step gain parameter for updating each preset coefficient of the multiplier of the one of said plurality of multiplication units, as recited in claim 1 and in combination with other elements of the claim.

Claims 1, 3 are allowed over prior art.

Art Unit: 2611

The prior art of the record fails to teach or suggest alone or in combination: A signal processing method, employing a second-order Volterra filter, for equalizing an input signal the signal processing method comprising: multiplying the first product signal and a signal output from each of said one or more series-connected delay units, each with a preset coefficient to produce a plurality of addend signals, multiplying the second product signal with a preset coefficient to produce an addend signal, summing the addend signal and the plurality of addend signal together, wherein a step gain parameter for updating the preset coefficient for multiplying the second product signal is twice a step gain parameter for updating each preset coefficient for multiplying the first product signal and the signal output form each of said one or more series-connected delay unit, as recited in claim 4 and in combination with other steps of the claim,

The prior art of the record fails to teach or suggest alone or in combination: A signal decoding apparatus, employing a second-order Volterra filter, for equalizing and decoding an input signal, the input signal decoding apparatus comprising: a multiplier configured to multiply a signal output from the one of said plurality of multiplication units and a signal output from each of said one or more series-connected delaying units, each with a preset coefficient, and a second adder configured to sum outputs of said multiplier together, wherein a step gain parameter for updating each preset coefficient of a multiplier of the remaining one of said plurality of multiplication units is twice a step gain parameter for updating each preset coefficient of the multiplier of the one of said

Page 4

Art Unit: 2611

plurality of multiplication units, as recited in claim 5 and in combination with other elements of the claim.

Claims 5,7, 13 are allowed over prior art.

The prior art of the record fails to teach or suggest alone or in combination: A signal decoding method employing a second-order Volterra filter in equalizing and decoding an input signal, the signal decoding method comprising: multiplying the first product signal and a signal output from each of said one or more series-connected delay units, each with a preset coefficient to produce a plurality of addend signals, multiplying the second product signal with a preset coefficient to produce an addend signal, summing the addend signal and the plurality of addend signal together, wherein a step gain parameter for updating the preset coefficient for multiplying the second product signal is twice a step gain parameter for updating each preset coefficient for multiplying the first product signal and the signal output form each of said one or more series-connected delay unit, as recited in claim 9 and in combination with other steps of the claim.

Claim 9 is allowed over prior art.

The prior art of the record fails to teach or suggest alone or in combination: A signal processing apparatus, comprising: a multiplier configured to multiply the signal output from the one of said plurality of multiplication units and a signal output from each of said one or more delay units each with a preset coefficient, and an adder configured to sum

outputs of said multiplier together, the remaining one of said plurality of multiplication units including, a shifter configured to left-shift the product signal to produce the signal output from the remaining one of said plurality of multiplication units, and a multiplier configured to multiply the signal output from the remaining one of said plurality of multiplication units with a preset coefficient, as recited in claim 10 and in combination with other elements of the claim.

Claim 10 is allowed over prior art.

The prior art of the record fails to teach or suggest alone or in combination: A signal processing apparatus, comprising: a multiplier configured to multiply the signal output from the respective of the one of said plurality of multiplication units and the one of the remaining ones of said plurality of multiplication units, and a signal output from each of said one or more delay units, each with a preset coefficient, and an adder configured to sum outputs of said multiplier together; a first adding unit configured to add an output from the adder of the one of the remaining ones of said plurality of multiplication units with an output from another of the remaining ones of said plurality of multiplication units to produce a summed signal; a shifter configured to left-shift the summed signal to produce a shifted signal; and a second adding unit configured to add the shifted signal and an output from the adder of the one of said plurality of multiplication units, as recited in claim 11 and in combination with other elements of the claim.

Claim 11 is allowed over prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Vis (U.S. 7,012,772)

Lee et al. (U.S. 5,471,504)

DeGroat et al. (U.S. 6,449,110)

Agarossi et al. (U.S. 6,600,794)

Fermo et al. "Simplified Volterra Filters for Acoustic Echo Cancellation in GSM Receivers" September 2000.

# **Contact Information**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SOPHIA VLAHOS whose telephone number is (571)272-5507. The examiner can normally be reached on MTWRF 8:30-17:00.

Application/Control Number: 10/555,096 Page 7

Art Unit: 2611

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammed Ghayour can be reached on 571 272 3021. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/SOPHIA VLAHOS/ Examiner, Art Unit 2611 11/12/2009

/Mohammad H Ghayour/ Supervisory Patent Examiner, Art Unit 2611